

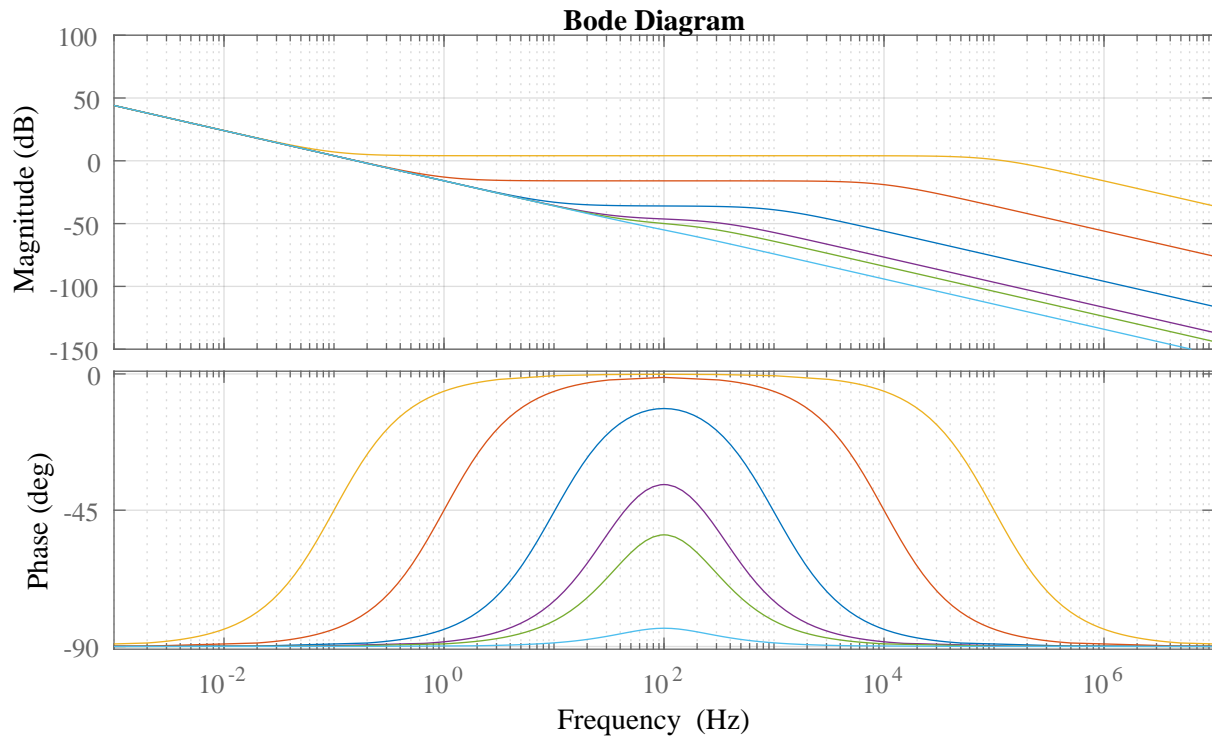
DIGITAL CONTROL OF POWER ELECTRONICS

Lead Lag Controller

The lead lag controller is shown below.

$$G_c(s) = \frac{k \frac{s}{\omega_z} + 1}{s \frac{s}{\omega_p} + 1} \quad (1)$$

Note, the controller has a gain k , an integrator (pole at zero) which is needed for zero steady state error, a zero at ω_z , and a pole at ω_p .



Select desired phase margin ϕ_{PM} , bandwidth f_c , and solve for f_z , f_p , and k_c .

$$\phi_{boost} = -90^\circ + \phi_{PM} - \angle G_{PS} \quad (2)$$

$$k_{boost} = \tan \left(45^\circ + \frac{\phi_{boost}}{2} \right) \quad (3)$$

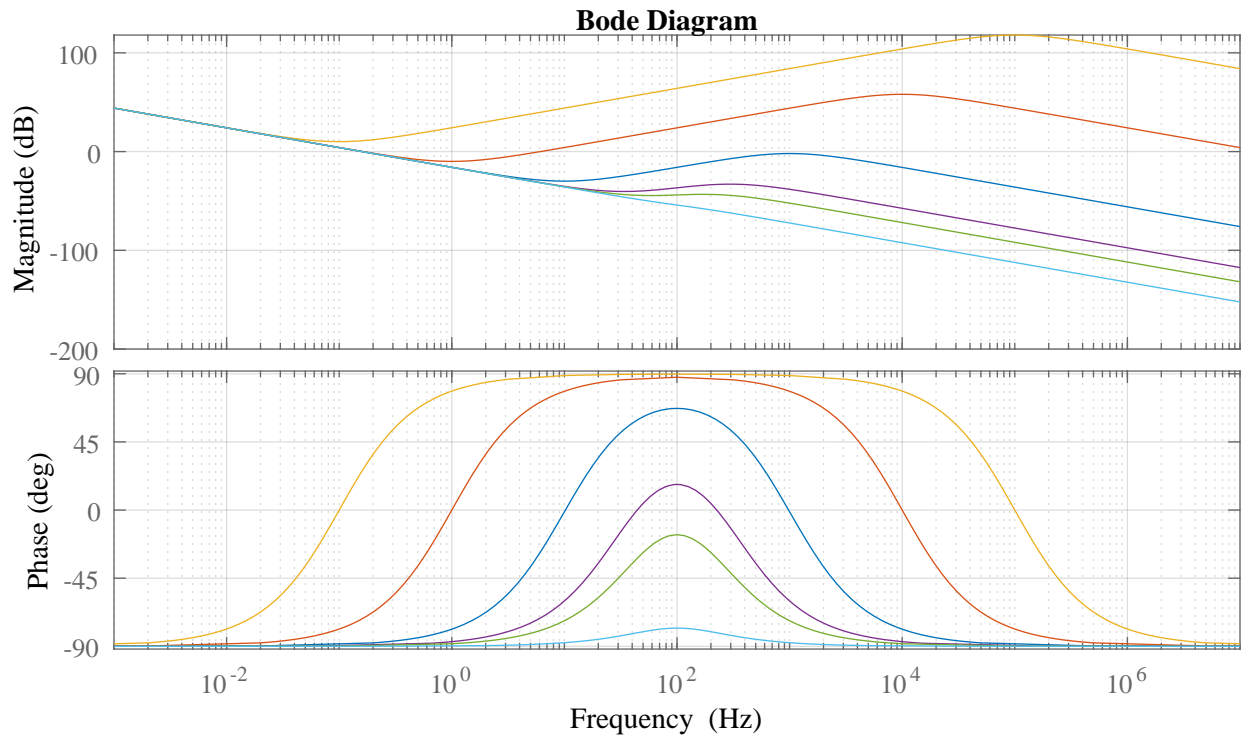
$$|G_c|_{f_c} = \frac{1}{|G_{PS}|_{f_c}} \quad (4)$$

$$f_z = \frac{f_c}{k_{boost}} \quad (5)$$

$$f_p = f_c k_{boost} \quad (6)$$

$$k_c = |G_c|_{f_c} \frac{2\pi f_z}{k_{boost}} \quad (7)$$

$$G_c(s) = \frac{k \left(\frac{s}{\omega_z} + 1 \right)^2}{s \left(\frac{s}{\omega_p} + 1 \right)^2} \quad (8)$$



$$\phi_{boost} = -90^\circ + \phi_{PM} - \angle G_{PS} \quad (9)$$

$$k_{boost} = \tan \left(45^\circ + \frac{\phi_{boost}}{4} \right) \quad (10)$$

$$|G_c|_{f_c} = \frac{1}{|G_{PS}|_{f_c}} \quad (11)$$

$$f_z = \frac{f_c}{k_{boost}} \quad (12)$$

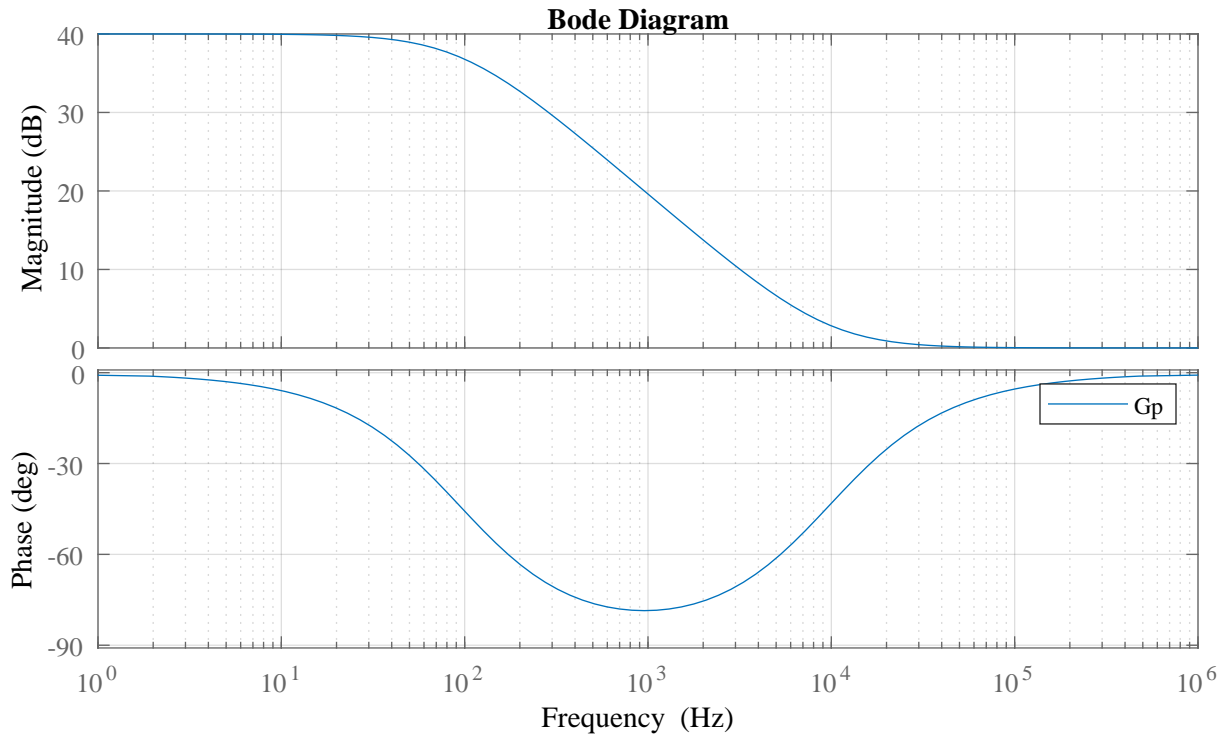
$$f_p = f_c k_{boost} \quad (13)$$

$$k_c = |G_c|_{f_c} \frac{2\pi f_z}{k_{boost}} \quad (14)$$

Lets assume we have the following plant

$$G_p(s) = \frac{(s + 60000)}{(s + 600)} \quad (15)$$

Design a lead lag controller for the plant with a bandwidth of 1000Hz and a phase margin of 60 degrees. The bode plot of the plant is shown below in blue. Find the gain and phase of the plant at 1000Hz.



$$|G_{PS}|_{f_c} = 9.558 \quad (16)$$

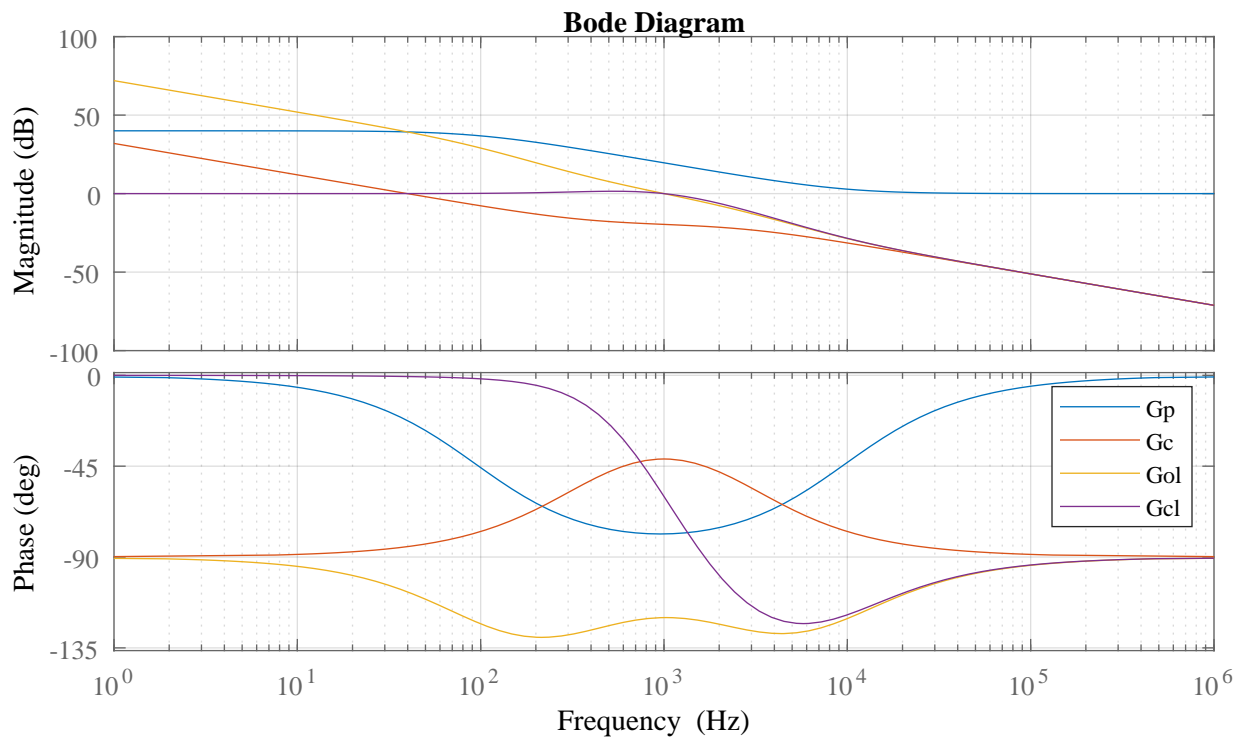
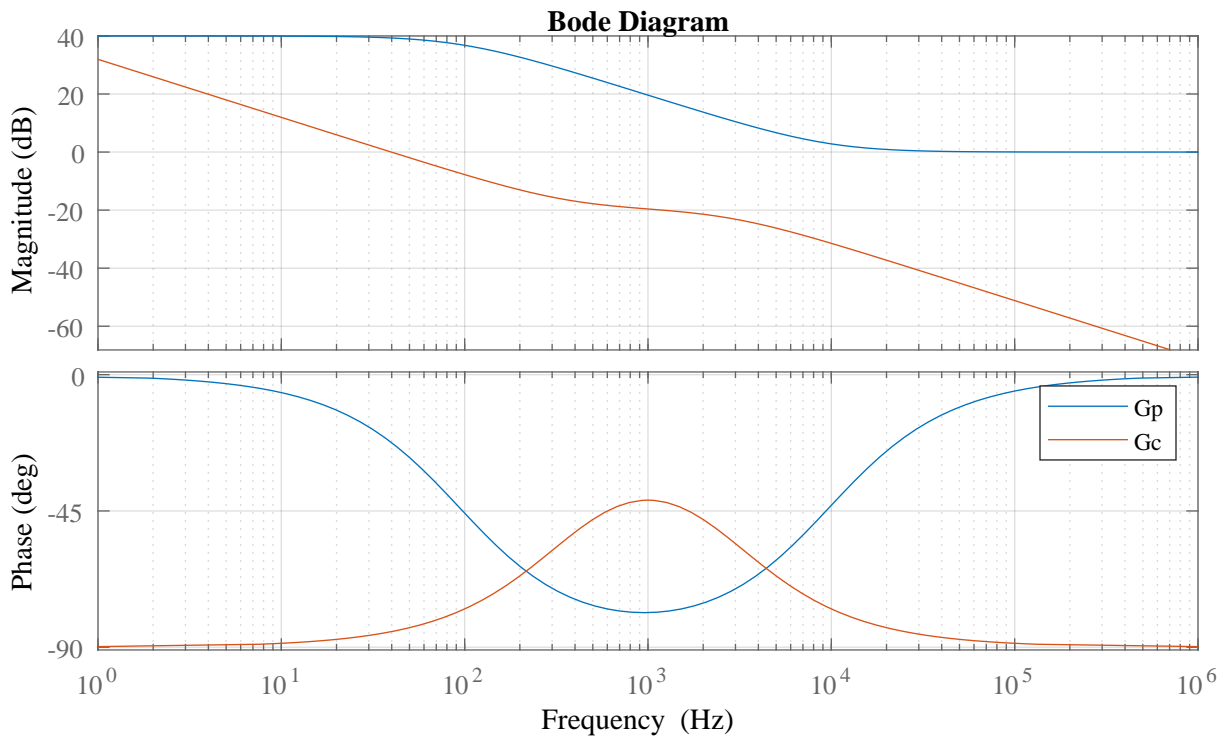
$$\angle G_{PS} = -78.567 \quad (17)$$

$$f_z = 378.1978 \quad (18)$$

$$f_p = 2644.1 \quad (19)$$

$$k_c = 248.6167 \quad (20)$$

$$G_c(s) = \frac{4.13 \times 10^6 s + 9.815 \times 10^9}{2376s^2 + 3.948 \times 10^7 s} \quad (21)$$



$$G_c(z) = \frac{0.05311z^2 + 0.01128z - 0.04183}{z^2 - 1.092z + 0.09249} \quad (22)$$

